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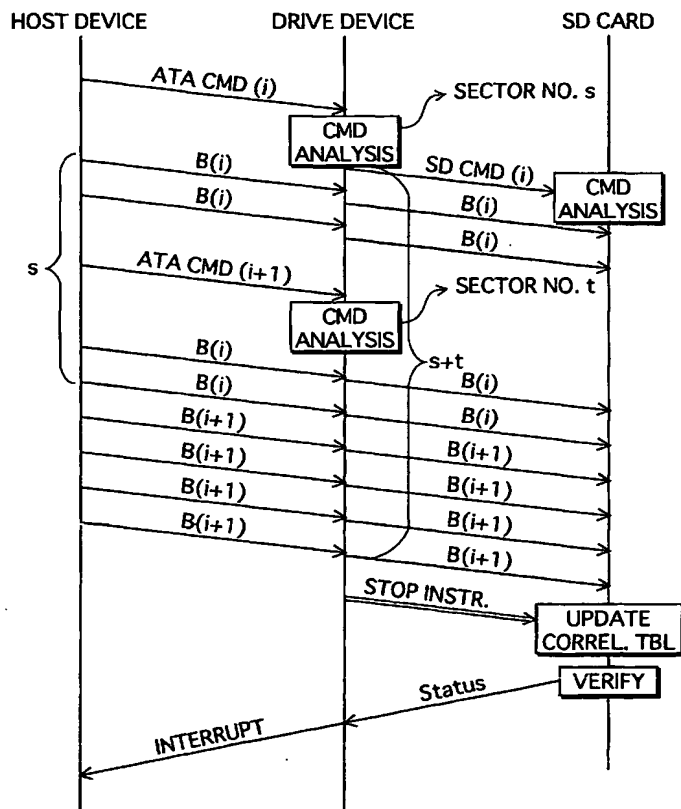
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- (71) Applicant (for all designated States except US): MATSUSHITA ELECTRIC INDUSTRIAL CO., LTD.  
[JP/JP]; 1006, Oazakadoma, Kadoma-shi, Osaka 5718501 (JP).
- (72) Inventor; and
- (75) Inventor/Applicant (for US only): OHTSUKA, Takeshi.
- (74) Agent: NAKAJIMA, Shiro; 6F, Yodogawa 5-Bankan, 2-1, Toyosaki 3-chome, Kita-ku, Osaka-shi, Osaka 5310072 (JP).
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(54) Title: DRIVE DEVICE AND RELATED COMPUTER PROGRAM



(57) Abstract: A drive device capable of preventing a drop in the data transfer rate, even when write instructions from a host device are given with commands having a short write data length. If the write end address of one of a plurality of ATA commands issued by a host device 2 is consecutive with the write start address of the next ATA command, a command analysis unit 11 has data writing to an SD card 1 by the consecutive commands performed in a single process. As a result, overheads when writing data to SD cards are incurred only once, allowing the transfer rate to be improved.

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